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ABSTRACT

SOXS will be the new spectroscopic facility for the ESO NTT telescope able to cover the optical and NIR bands by using two different arms: the UV-VIS (350-850 nm), and the NIR (800-2000 nm). In this article, we describe the development status of the visible camera cryostat, the architecture of the acquisition system and the progress in the electronic design. The UV-VIS detector system is based on a CCD detector 44-82 from e2v, a custom detector head, coupled with the ESO continuous flow cryostats (CFC), a custom cooling system, based on a Programmable Logic Controller (PLC), and the New General Controller (NGC) developed by ESO. This paper outlines the development status of the system, describes the design of the different parts that make up the UV-VIS arm and is accompanied by a series of information describing the SOXS design solutions in the mechanics and in the electronics parts. The first tests of the detector system with the UV-VIS camera will be shown.

(Ref. 1-28).

Keywords: spectrograph, UV-VIS, detector, Control System, CCD

1. THE UV-VIS DETECTOR SYSTEM

The UV-VIS CCD Detector System for SOXS is based on the e2v CCD44-82, the ESO NGC CCD controller unit and an ad hoc designed camera.

1.1 The Detector

The e2v detector is a back illuminated CCD with a $15\ \mu$ square pixel and an image area of 30.7 x 61.4 mm. The high quantum efficiency (QE) in the spectral response of the spectrograph (350-850 nm) and their geometric characteristics (pixel size and dimensions) make this CCD the most suitable for our instrument.

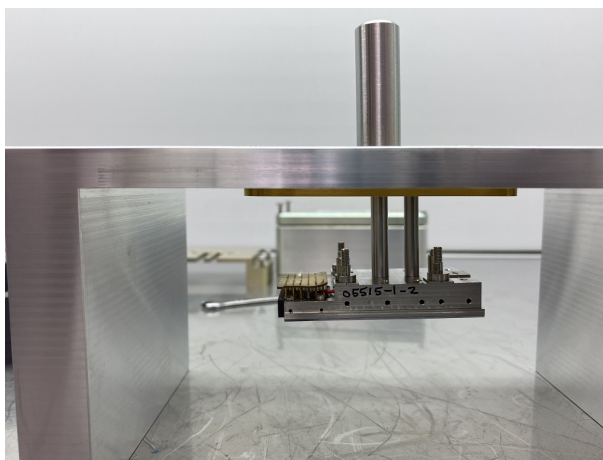


Figure 1. The engineering CCD on the mounting facilities hardware.

1.2 The NGC controller

The ESO CCD controller (NGC) is a programmable electronic system for the UV-VIS CCD Detector polarization and readout. The flexibility in the programming of the operative parameters (bias and clock voltages, clock sequences, electronic gain) of the CCD and the features provided for the testing of the signals (programmable test points on the NGC front panel) helps the realization of the test and the optimization of the CCD operative conditions. In addition, the NGC is fully compatible with ESO software and it makes it easier the integration of the SOXS spectrograph in the software NTT environment.

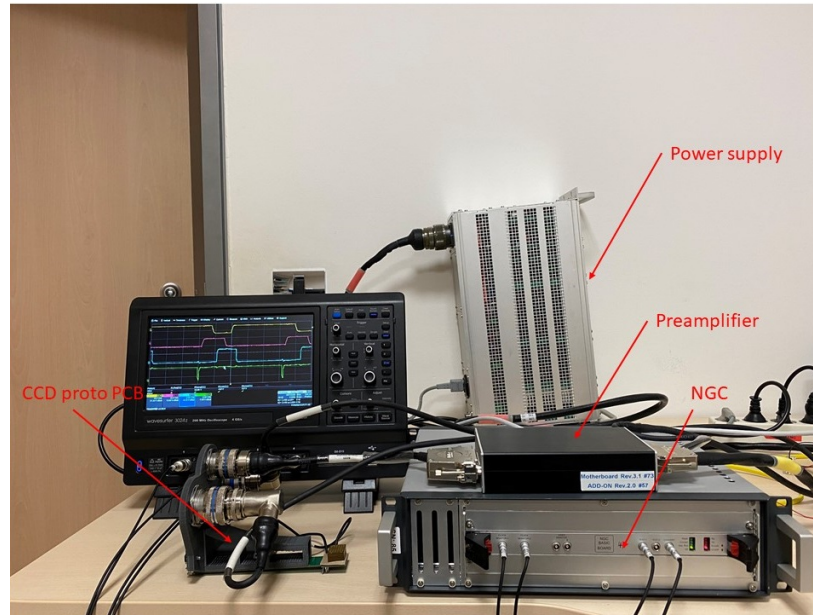


Figure 2. The CCD test bench with the NGC and test facilities.

1.3 The camera housing

The camera is made up by the cryogenic part, based on the Continuous Flow Cryostat (CFC) coupled with the UV-VIS camera and the wiring for the detector and for the temperature sensors and control. The mechanic design of the camera is optimized to be mounted inside the reduced dimension of the UV-VIS spectrograph with the consequence of a reduced space inside the camera for the electronic connections. The reduced space inside the camera led to the development of an optimized Printed Circuit Board (PCB), based on flex technology, for the connection of the CCD with the front-end electronics (Figure 4).

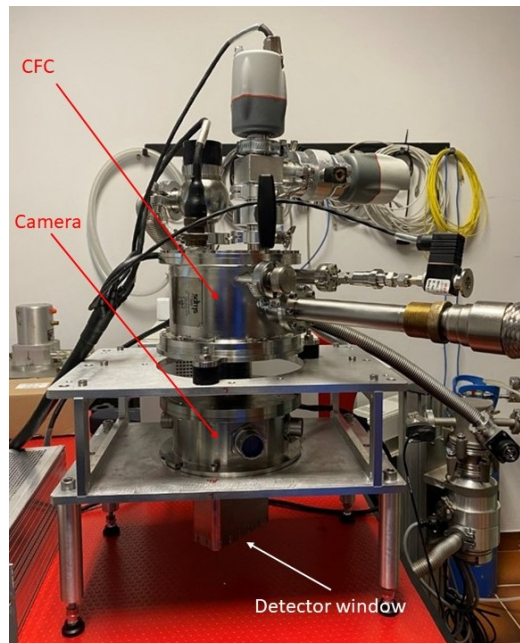


Figure 3. UV-VIS camera coupled with CFC.

1.4 The internal wiring

For the internal wiring we adopted a solution based on a flex cable for the CCD connection, a coaxial cable for the video signals and a twisted vacuum wires for the temperature reading and control.

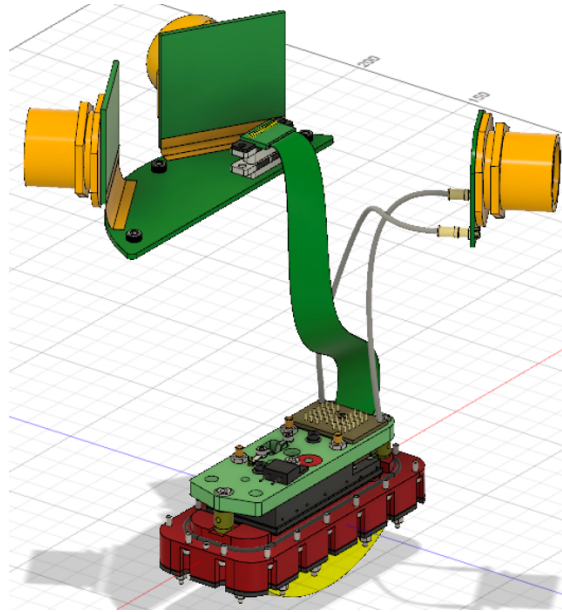


Figure 4. design of the CCD flex boards.

2. UV-VIS LABORATORY TESTS

The first test, to optimize the connections and minimize the noise, was done with a prototype PCB (Figure 9) and with a laboratory CCD housing (Figure 10). In these test we measured the CCD clocks, the bias and the video signals.

2.1 Electronic tests

The measures of the clocks signals shown a correct temporization of the phases and the good quality of the signals on the Zero Insertion Force (ZIF) socket of the CCD. Figure 5 and Figure 6 shown the CCD phases measured in the CCD board (on the ZIF socket).

The preamplifier was tested with a signal generator at different frequency to test the programmable bandwidth settings and the corresponding electronic gains (Figure 7).

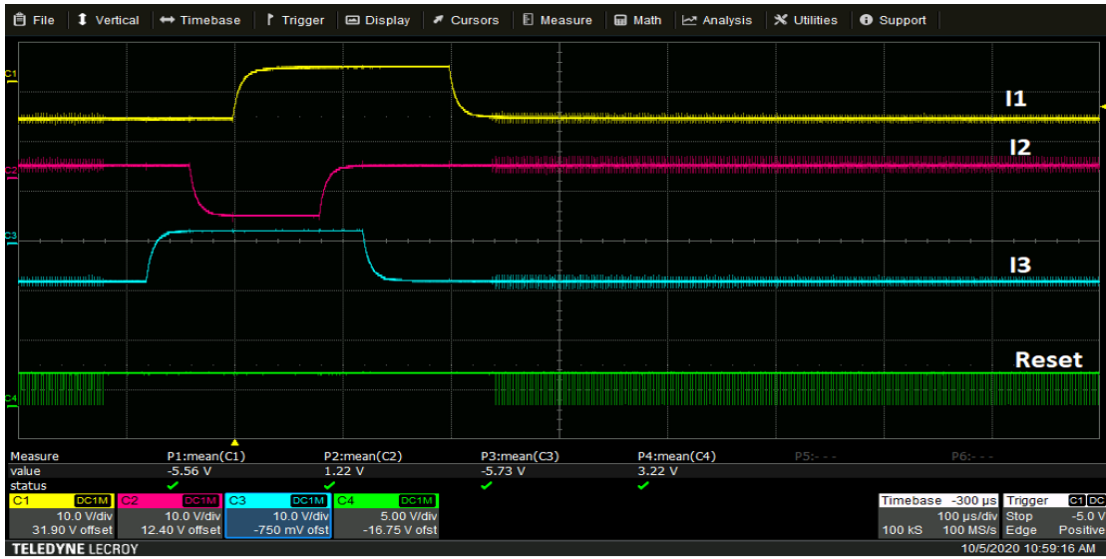


Figure 5. CCD vertical phases

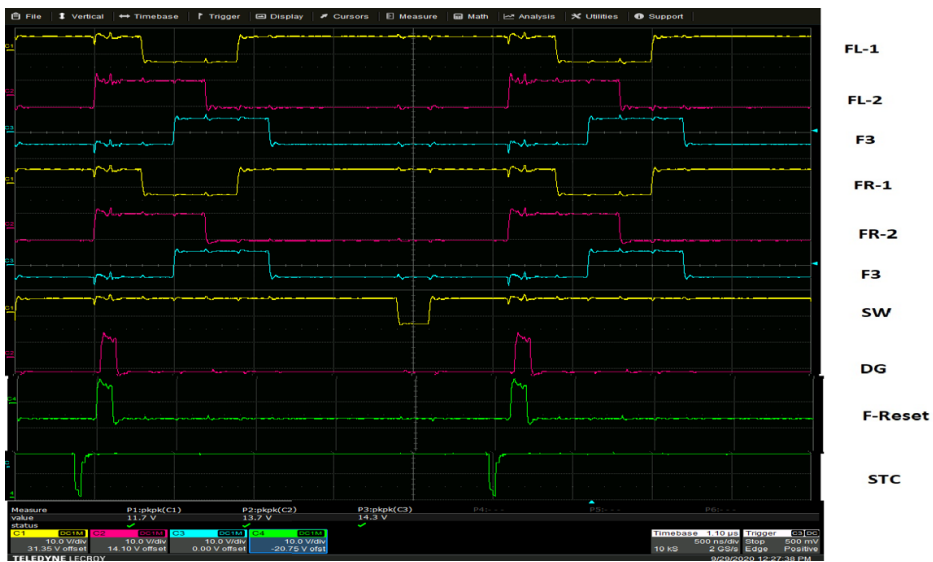


Figure 6. CCD horizontal phases and control timing.

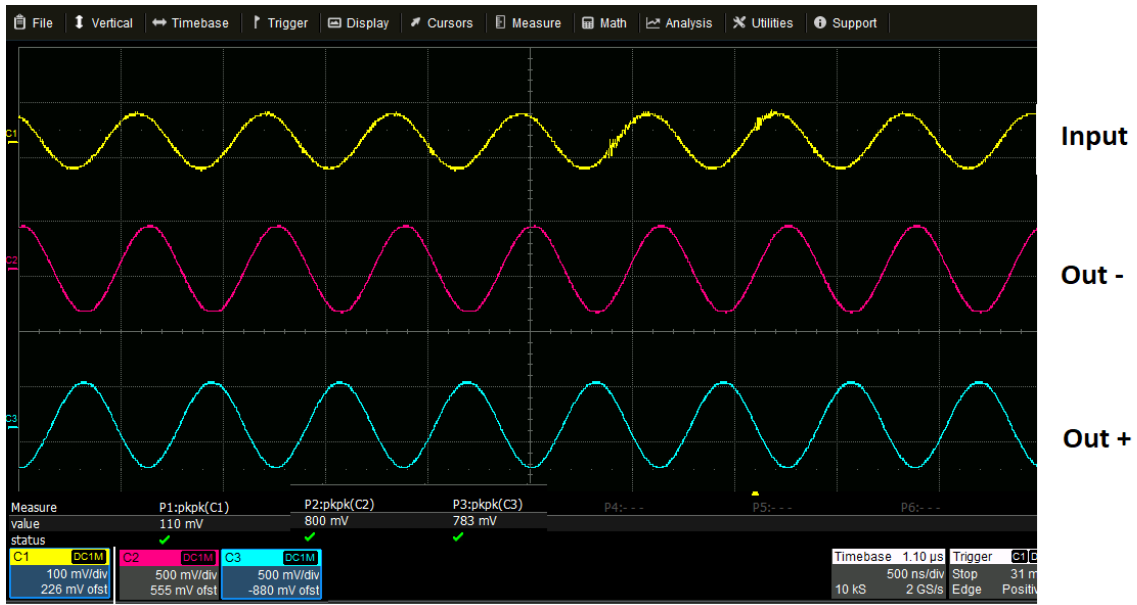


Figure 7. Preamplifier test with an external signal.

2.2 Detector tests

The engineering CCD was tested with the test bench shown in Figure 2. We chose the buffered CCD output OP and the preamplifier input as shown in Figure 8.

Detail of FET Buffer

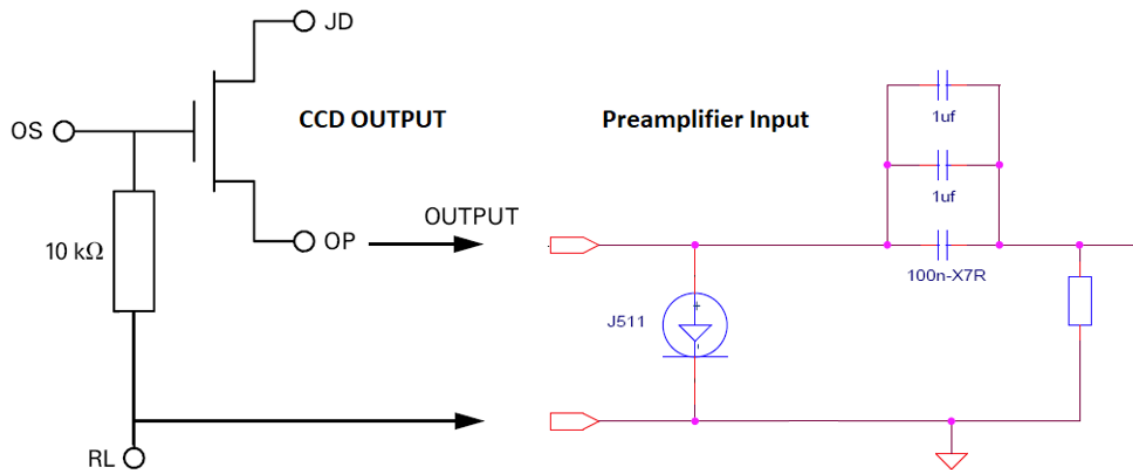


Figure 8. CCD output and preamplifier input.

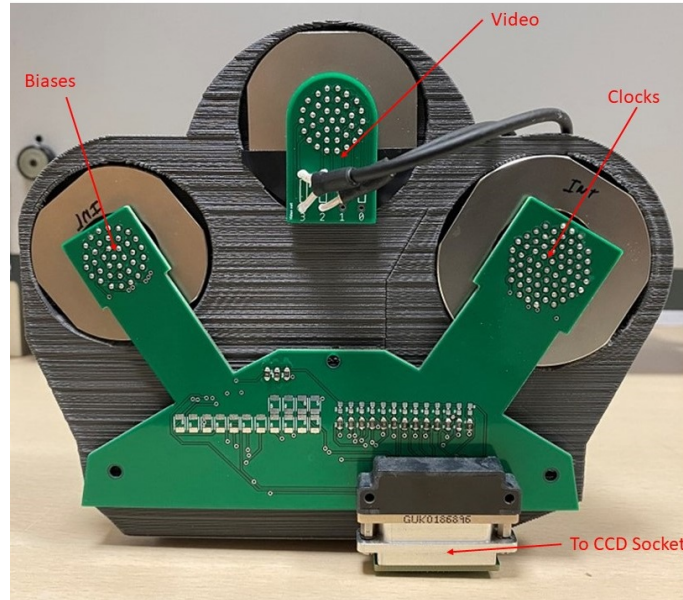


Figure 9. CCD board prototype.

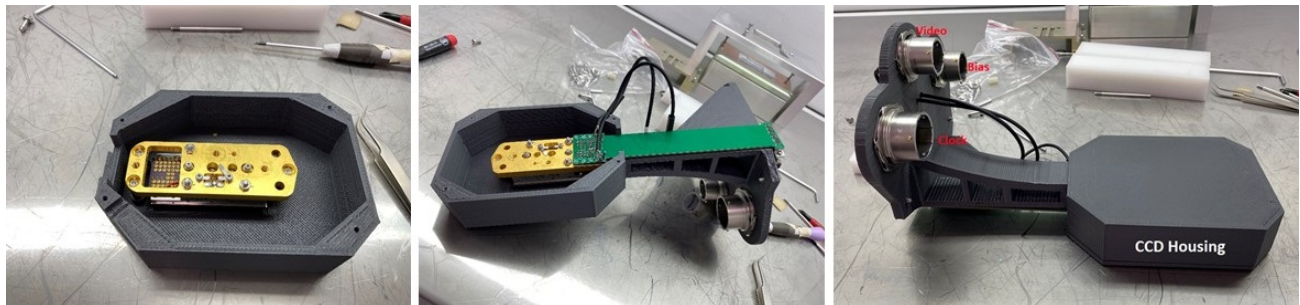


Figure 10. CCD test bench (for CCD warm tests)

The tests have been done with an engineering CCD and at room temperature. In this condition is possible to verify if the acquisition system works as expected, because the CCD image has to show the overscan and the expected thermal signal. We have adjusted the video offsets and the gain and we acquired three images, one for each readout mode. In Figure 11 are shown the CCD images, acquired in the three readouts mode available (Left-Right, Left and Right). In these images the overscan is clearly visible and the images shows the thermic electrons. In this engineering CCD seems that the serial register is broken, because in the images and with more evidence in the horizontal cuts (Figure 12) part of the CCD did not show the thermal signal.

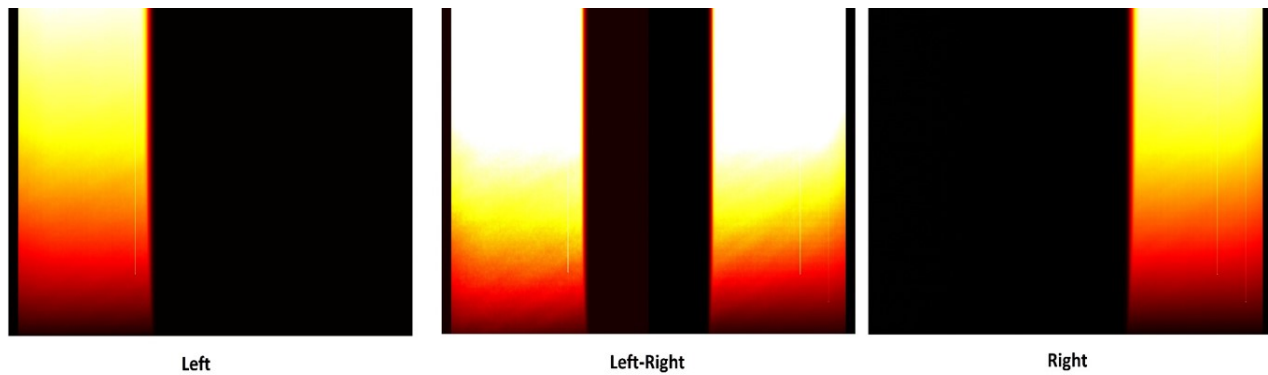


Figure 11. CCD images (room temperature).

The acquired images shown that the thermal signal increase with a gradient trend as expected (Figure 12)

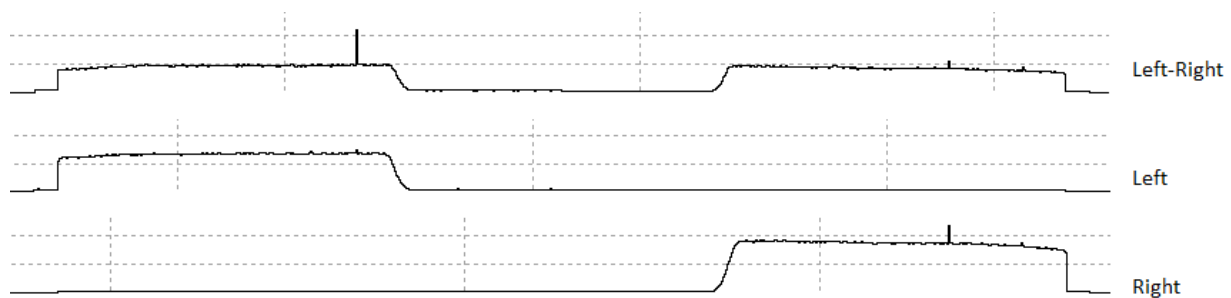


Figure 12. CCD vertical cuts.

More optimizations, at cryogenic temperature, will be done with the SOXS UV-VIS camera, the final flex PCB and the scientific CCD.

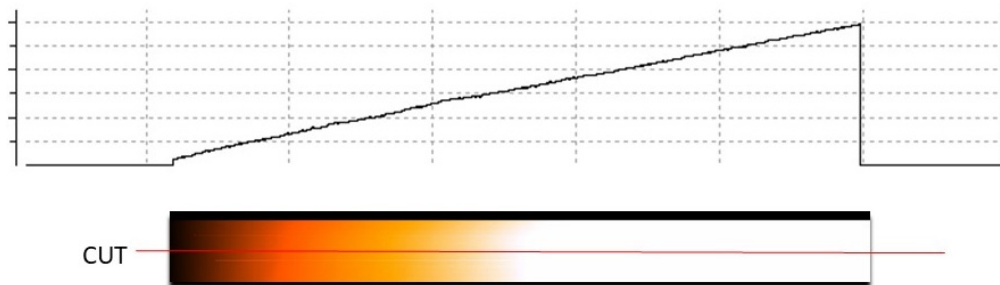


Figure 13. CCD horizontal cuts.

2.3 Vacuum tests

The preliminary vacuum test was done with the dummy CCD and without the CCD wiring. Leak test are foreseen for mid December 2020.

2.4 Cryogenic tests

The cryogenic test was done with satisfactory results. The CFC shown a low LN2 consumption in a 10 days continuous cooling (less than 0.5 l/hr) and the equilibrium CCD temperature reach the expected values.

2.5 Conclusions

The tests on the prototype PCBs with the engineering CCD shown that the acquisition system works as expected. The next step will be the final test at cryogenic temperature with the scientific CCD in order to optimize the CCD settings and reach the optimum performance in term of thermic stability of the detector, minimum readout noise and data throughput.

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